WHAT IS CLAIMED IS:

1. An image processing system provided with a field programmable gate array which is capable of altering an internal logic description prescribing operation during operating state, wherein an image processing method of said image processing system comprising the steps of:

executing digital image processing of interval of active pixel in the condition that a first internal logic description is written in said field programmable gate array;

executing digital control processing in the condition that said first internal logic description of said field programmable gate array is rewritten to a second internal logic description in interval of non-active pixel with the exception of said interval of active pixel; and

executing digital image processing again in the condition that said second internal description is rewritten to said first internal logic description.

2. An image processing system as claimed in claim 1, wherein there is provided an image pick-up element, and said image processing system executes color signal processing of picked-up image by said image pick-up element during said interval of active pixel, while during said interval of non-active pixel, said image processing system executes said digital control processing in relation to said color signal processing.

3. An image processing system as claimed in claim 1, wherein said interval of non-active pixel is a vertical blanking interval.

4. An image processing system as claimed in claim 1, wherein said interval of non-active pixel is a horizontal blanking interval.

5. An image processing system as claimed in claim 1, wherein said image processing system executes image compression processing in said interval of active pixel, and said image processing system executes digital control processing in relation to said image compression processing in said interval of non-active pixel.

6. An image processing system as claimed in claim 1, wherein said digital control processing is code quantity control processing.

7. An image processing system as claimed in claim 2, wherein said interval of non-active pixel is a interval of optical black pixel of said image pick-up element.

8. An image processing system as claimed in claim 2, wherein said digital control processing is an automatic white balance control processing.

9. An image processing system as claimed in claim 2, wherein said digital control processing is an auto-focus control processing.

10. An image processing system as claimed in claim 2, wherein said digital control processing is an automatic lightness control processing.

Add A2
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